

New BERTScope™ CRJ Clock Recovery Instrument with Jitter Analysis Capability

MENLO PARK, CA, January 30, 2007 – SyntheSys Research, Inc., a developer and manufacturer of high speed signal integrity test and measurement solutions for the computer, storage, and communications industries, introduces the new BERTScope CRJ clock recovery instrument with jitter analysis capability.

When used with a BERTScope signal integrity analyzer, the BERTScope CRJ directly measures jitter components from the serial input data while simultaneously generating a recovered clock. The measurements include a spectral analysis of jitter components, along with a direct measure of duty cycle dependent jitter.

The jitter spectrum view allows users to quickly validate their device compliance to serial data standards, such as PCI-E Gen2, which specify a total jitter value over a band-limited range of frequency.

“In the past when engineers were working with lower speed serial data standards, clock recovery was performed in a simple module with no controls,” says Steve Sekel, Product Manager for clock recovery products at SyntheSys Research. “However, the phase locked loop characteristics such as bandwidth, peaking and roll off rate directly affect the amount of jitter removed by the clock recovery process. With channel data rates now reaching several Gb/s, measurement errors of only a few tens of picoseconds can represent a large percentage of a unit interval, and must be eliminated. Precise emulation of the PLL characteristics in the system receiver, or specified by the compliance standard ‘Golden PLL,’ are essential and cannot be achieved with the simple clock recovery modules.”

The new instrument is based on the BERTScope CR, the highest performance instrumentation-grade adjustable clock recovery product available today. The precision PLL used in the BERTScope CR and CRJ allows the user to set the loop characteristics of bandwidth, roll off slope, and peaking, allowing precise emulation of the system receiver characteristics. For verification purposes, the BERTScope CR and CRJ can measure their actual loop bandwidth and jitter transfer function, and present this information in a graphical display.

In addition to being used with the BERTScope, the BERTScope CR and CRJ clock recovery functions operate stand-alone, for providing the same unmatched performance with sampling oscilloscopes and other bit error rate test instruments.

Price and Availability

The US List price for the BERTScope CRJ starts at \$39,500. Availability is 10 weeks ARO.

About SyntheSys Research, Inc.

SyntheSys Research, Inc., innovator of the BERTScope™, is a privately held corporation located in Menlo Park, California. Founded in 1989, its mission is to develop advanced test instruments for identifying and locating the source of errors in high-speed digital bit streams. The BERTScope pairs with BERTScope CRJ to offer the vision of a scope, the confidence of a BERT, and clock recovery you can count on. More information is available at www.bertscope.com.

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