

First SONET/SDH/OTN Jitter Compliance Solution Showing Jitter Spectrum to 80 MHz

MENLO PARK, CA, September 16, 2007 – SyntheSys Research, Inc., introduces the new BERTScope™ Digital Communications Receiver with Jitter Analysis (DCRj) – the first 10 Gb/s telecom jitter solution to test device compliance and show the full 80 MHz spectrum to precisely see and troubleshoot device behavior. Aimed at telecom applications, the DCRj will also be valuable to developers of XFP and SFP+ transceivers.

The DCRj is a standalone jitter measurement solution with built-in compliant clock recovery that also provides OC-192/STM-64 and 10GbE reference receiver capability to the BERTScope series of analyzers and third-party instruments used for transmitter compliance. Offering broad wavelength coverage, the DCRj has an amplified receiver for testing single-mode or multimode transmitters. The DCRj also provides an electrical input to enable jitter spectral analysis on electrical signals. While the majority of the optical or electrical signal is passed through to provide an electrical output for other test instrumentation, a small portion is used to drive a standards-compliant, flexible clock recovery. As well as providing the correct loop bandwidths for transmitter measurements, such as the 4 MHz loop bandwidth called for by SONET, the DCRj also provides the engine for one of the most sensitive jitter generation measurements available. The benefit is that test devices may be checked for compliance to standards, and the same measurements can be used for performance enhancement and troubleshooting efforts through the >80 MHz jitter spectrum measurement.

Combining the DCRj with the BERTScope S 12500B signal integrity analyzer, SONET/SDH and 10GbE transmitter compliance measurements become easy, as does the ability to look deeper into device performance. Whether calibrating a stressed eye or measuring a compliance mask at 10^{-12} BER levels, the DCRj is the perfect complement to the BERTScope for greater insight in optical serial data applications. The stressed output of the BERTScope and DCRj enables jitter transfer and jitter tolerance measurements, providing the complete suite of telecom jitter measurements.

Visit the BERTScope Booth 17114 at ECOC in Berlin to see a live demo of the new DCRj.

Availability

The BERTScope 11000A Digital Communications Receiver with Jitter Analysis is available 12 weeks ARO.

About SyntheSys Research, Inc.

Founded in 1989, SyntheSys Research, Inc., innovator of the award-winning BERTScope™, is a privately held corporation located in Menlo Park, California. SyntheSys develops and manufactures high-speed signal integrity test and measurement instrumentation for the computer, storage and communications industries. www.bertscope.com

Contact: Jill Hagaman, jill_hagaman@bertscope.com, 650-364-1853

