

Faster-to-Market 100 Gigabit Ethernet Components with New BERTScope™ 26 Gb/s Analyzer and Clock Recovery

MENLO PARK, CA, October 30, 2009 – SyntheSys Research, Inc. announces the availability of two new products to enable the burgeoning 100 Gigabit Ethernet industry. Building on the already market-leading BERTScope physical layer generator and BER analyzer, the new 25000 models enable semiconductor, transceiver, and system engineers to stimulate and analyze performance of critical 25.78125 Gb/s Ethernet and OIF CEI building blocks. The ideal companion to this new BERTScope is the new clock recovery instrument with equalization, the CR 25000A.

100 Gigabit Ethernet developments are running behind user demand for bandwidth. To accelerate deployment, engineers need to validate designs to the still-emerging Ethernet and Common Electrical I/O (CEI, from the Optical Networking Forum) parameters. Electrical performance, particularly jitter, is critical. The new BERTScope provides flexible jitter tolerance testing of receivers to 26 Gb/s, going beyond compliance to give insight into performance limits and design troubleshooting. With optional Jitter Map capability, it can provide in-depth analysis of transmitter jitter, including deterministic jitter subcomponents and triangulation on long compliance patterns such as pseudo-random binary sequence PRBS-31.

Many test engineers that need to make physical layer measurements also need clock recovery, either because they have no clock available, or they need to make measurements with compliant loop bandwidths. The BERTScope CR 25000A fulfills these needs with all the flexibility in loop bandwidth and peaking that engineers require, to 26 Gb/s. At speeds above 10 Gb/s, circuit board materials can cause appreciable distortion to transmitted bits due to frequency dependent loss. The CR 25000A includes a linear equalizer applying to the clock recovery circuit, to allow successful lock even on distorted eye diagrams that are closed from such effects. The level of equalization may be varied by the user to allow for a wide range of inter-symbol interference (ISI).

The BERTScope 25000 is available in two models, the 25000C Si with stressed eye generation, and the 25000A without. The BERTScope CR 25000 is also available in two models, one with a data through path usable to 12.5 Gb/s (CR 25000A), the other with a direct input for higher sensitivity applications (CR HS 25000A).

Availability

The BERTScope 25000A, Si 25000C, and clock recovery CR 25000A are available 8 weeks ARO.

About SyntheSys Research, Inc.

Founded in 1989, SyntheSys Research, Inc., innovator of the award-winning BERTScope™, is a privately held corporation located in Menlo Park, California. SyntheSys develops and manufactures high-speed signal integrity test and measurement instrumentation for the computer, storage, and communications industries. Learn more at www.bertscope.com.

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