

Fastest Instrument-Grade Clock Recovery Released, the BERTScope™ CR 28000A, for Testing OIF-CEI 28 Gb/s Links

MENLO PARK, CA, January 27, 2010 — SyntheSys Research, Inc., announces the availability of the newest member of the BERTScope Clock Recovery family, the CR 28000A, with operation to 28.6 Gb/s. Aimed at the fast growing 100 Gb/s Ethernet (GbE), OIF-CEI and anticipated 32G Fibre Channel markets, the new model has all of the flexibility already established by the market leading BERTScope CR products, such as flexible loop bandwidth and peaking. However, it adds a unique capability aimed at overcoming issues that all such high speed links are likely to face - deriving clock from an incoming data stream that is completely closed by dispersion from Inter-Symbol Interference (ISI).

The passage of high speed signals over circuit boards has many challenges, including the restricted bandwidth of many transmission lines; this leads to distortion of the signal, and what is commonly referred to as a 'closed eye'. The CR 28000A is uniquely equipped with a linear equalizer as part of its circuitry that allows a user-selectable amount of correction to be applied, enabling a high quality clock to be derived for measurement purposes and general lab use.

Availability

The BERTScope CR 28000A is available 8 weeks ARO.

About SyntheSys Research, Inc.

Founded in 1989, SyntheSys Research, Inc., innovator of the award-winning BERTScope™, is a privately held corporation located in Menlo Park, California. SyntheSys develops and manufactures high-speed signal integrity test and measurement instrumentation for the computer, storage and communications industries. Learn more at www.bertscope.com.

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