

Fast USB 3.0 Compliance with New BERTScope™ Si 8500C

MENLO PARK, CA, January 27, 2010 — SyntheSys Research, Inc. announces the availability of the newest serial data analyzer addition to the BERTScope family, the 8.5 Gb/s BERTScope Si 8500C. Enabling test and characterization of the newest standards such as USB 3.0 and PCI Express 8.0 GT/s, the 8500C contains comprehensive stress generation for receiver jitter tolerance testing including spread spectrum clocking (SSC) generation. In addition, this new model BERTScope includes asynchronous BER test capability for USB 3.0, SATA, PCI Express, and other 8b/10b encoded systems requiring receiver test with differing clock domains.

The 8500C builds on the award winning BERTScope 7500B analyzer increasing the bit rate coverage and bringing new features such as extended and more flexible pattern memory for easy loopback initiation, tighter user control over some eye diagram measurements for improved automation of compliant measurements, and enhanced waveform performance.

Pair this latest BERTScope with the compliant Jitter Transfer Function in the instrument-grade BERTScope CR 12500A for USB 3.0 transmitter measurements, and use the BERTScope DPP 12500A Digital Pre-Emphasis Processor providing adjustable de-emphasis for compliant USB 3.0 receiver test. These products, as well as the BERTScope's integrated stress generation, are seamlessly integrated into the BERTScope's software for fast setup and a 'single-instrument' experience.

In addition, the new instrument offers enhanced waveform performance, flexible reference clocking and improved connectivity. Optionally, PCI Express-specific stresses can be added, as well as the ability to stress live data traffic. The latter is particularly useful for line card and motherboard designers wishing to exercise system receivers in situations where a working device cannot pass simple test patterns.

Availability

The BERTScope Si 8500C is available 8 weeks ARO.

About SyntheSys Research, Inc.

Founded in 1989, SyntheSys Research, Inc., innovator of the award-winning BERTScope™, is a privately held corporation located in Menlo Park, California. SyntheSys develops and manufactures high-speed signal integrity test and measurement instrumentation for the computer, storage and communications industries. Learn more at www.bertscope.com.

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